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DTH 8-5-03#14/C NIE.  
M.H.  
8-4-03  
PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant : Peter Warnes, et al.  
Appl. No. : 09/523,877  
Filed : March 13, 2000  
For : **METHOD AND APPARATUS  
FOR MULTI-MODE JUMP  
DELAY SLOT CONTROL IN  
A PIPELINED PROCESSOR**  
Examiner : Huisman, David J.  
Group Art Unit: 2183



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Technology Center 2100

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I hereby certify that this correspondence  
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Commissioner for Patents, P.O. Box  
1450, Alexandria, VA 22313-1450, on

July 29, 2003

(Date)

  
Robert F. Gazdzinski  
Reg. No. 39,990AMENDMENT AND RESPONSE TO OFFICE ACTION

10 Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Dear Sir:

In response to the Office Action dated May 29, 2003 for the above-identified application  
("Office Action"), Applicant provides the following: